

BEST AVAILABLE COPY**PATENT NUMBER and
ISSUE DATE****U.S. UTILITY Patent Application**

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|---------------------------------------|---------------------------|--------------|-----------------|-------------|----------------------------------|
| APPL NUM 10078516 | FILING DATE 02/21/2002 | CLAS8 438 | SUBCLASS 263 | GAU 2012 | EXAMINER <i>Peter Jackson</i> |
| **APPLICANTS: Rozsypal Antonin; 127-2 | | | | | |

****CONTINUING DATA VERIFIED:****** FOREIGN APPLICATIONS VERIFIED:**

| | | | |
|---|---|--|--------------------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed 35 USC 119 conditions met | | <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO ONS00301 |
| Verified and Acknowledged Examiners's initials | | | |
| TITLE : CMOS current mode RF detector and method | | | |
| U S DEPT. OF COMM /PAT & TM-PTO-436L (Rev. 12-04) | | | |

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|--|-----------|----------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Assistant Examiner | |
| ISSUE FEE | | Primary Examiner | |
| Amount Due | Date Paid | Total Claims | Print Claim for O.G. |
| TERMINAL DISCLAIMER | | DRAWING | |
| | | Sheets Drwg. | Figs.Drwg. |
| | | Print Fig. | |
| PREPARED FOR ISSUE | | Application Examiner | |
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